



#5 02-09-04
2685

7217/65190

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

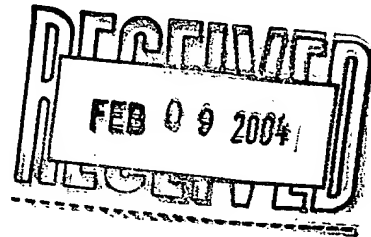
Applicants : Taiwa Okanobu
Serial No. : 09/921,421
Filed : August 2, 2001
For : ANTENNA UNIT AND RECEIVING CIRCUIT

RECEIVED
FEB 06 2004
Technology Center 2600

I hereby certify that this paper is being
deposited this date with the U.S. Postal
Service as first class mail addressed to:
Commissioner for Patents,
PO Box 1450
Alexandria, VA 22313-1450

Jay H. Maioli
Reg. No. 27,213

Date
Jan 30, 2004



January 30, 2004
1185 Avenue of the Americas
New York, NY 10036
(212) 278-0400

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(c)

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

As a means of complying with the duty of disclosure set forth in 37 CFR § 1.53 and in keeping with the guidelines of 37 CFR 1.98, Applicants hereby submit information thought to be relevant to the examination of the above-identified application, Also submitted herewith is a completed form PTO-1449.

This information was cited in a European Search Report

dated January 16, 2004, and it is hereby certified that this disclosure is being made within three months of that date.

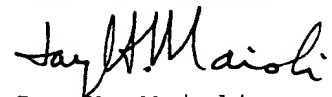
United States Patent 5,966,638, Mita et al., relates to a receiving antenna and receiving system that are simply configured, low priced and capable of making a parabola antenna oriented toward a predetermined satellite among a plurality of existing satellites.

European Patent Application EP 0847135 A2, Abe et al., relates to a high frequency variable gain amplifier device that uses minimal power using a plurality of transistor amplifiers wherein when a power supply voltage is supplied to each of the transistor amplifiers via a change-over switch circuit, the field effect transistor is turned "off" and when the power supply voltage is interrupted, the field effect transistor switch is turned "on".

PCT Application WO 00/08751 A1, Black, relates to an automatic gain control circuit (AGC) for controlling multiple variable gain amplifier stages while estimating received signal power. The AGC circuit being generally configurable to accommodate a variety of AGC amplifier configurations to enhance IP3 performance and reduce required amplifier current, while providing a received power estimate which remains valid regardless of how the gain or attenuation is distributed among the various amplifiers.

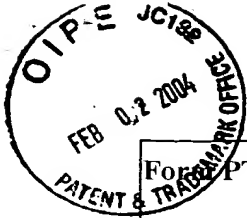
Respectfully submitted,

COOPER & DUNHAM LLP

A handwritten signature in black ink, reading "Jay H. Maioli". The signature is written in a cursive style with a large, stylized "M".

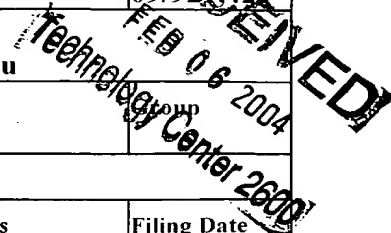
Jay H. Maioli
Reg. No. 27,213

JHM/JBG
Encl.



Sheet 1 of 1

Form PTO-1449

U.S. Department of Commerce
Patent and Trademark OfficeAtty. Docket No.
7217/65190Serial No.
09/926,321INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)Applicants
Taiwa OkanobuFiling Date
August 2, 2001

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	US 5 9 6 6 6 3 8	10/12/99	Mita et al.	455	6.3	

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	EP 0 8 4 7 1 3 5	6/10/98	European	H03G 3	30		
	WO 0 0 0 8 7 5 1	2/17/00	PCT	H03G 3	20		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.